

Fig. 1

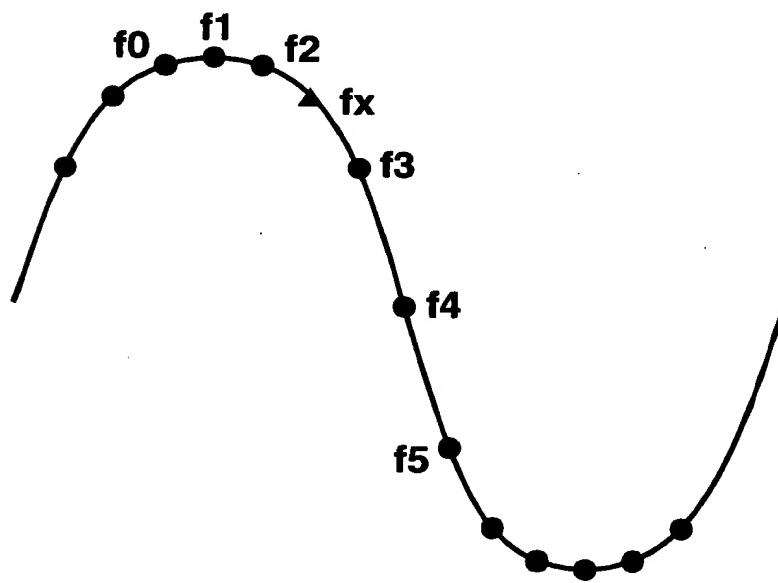


Fig. 2

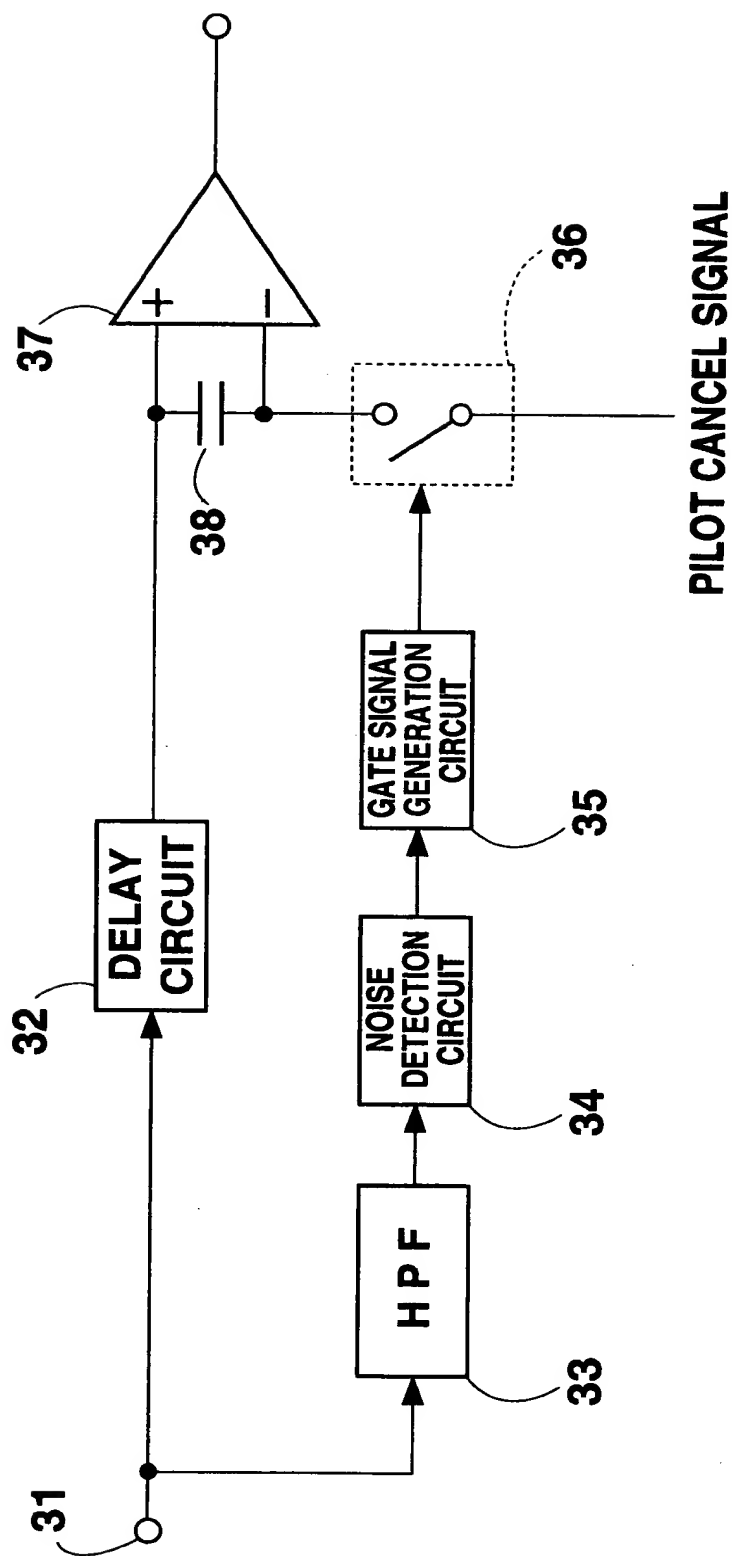


Fig. 3 PRIOR ART

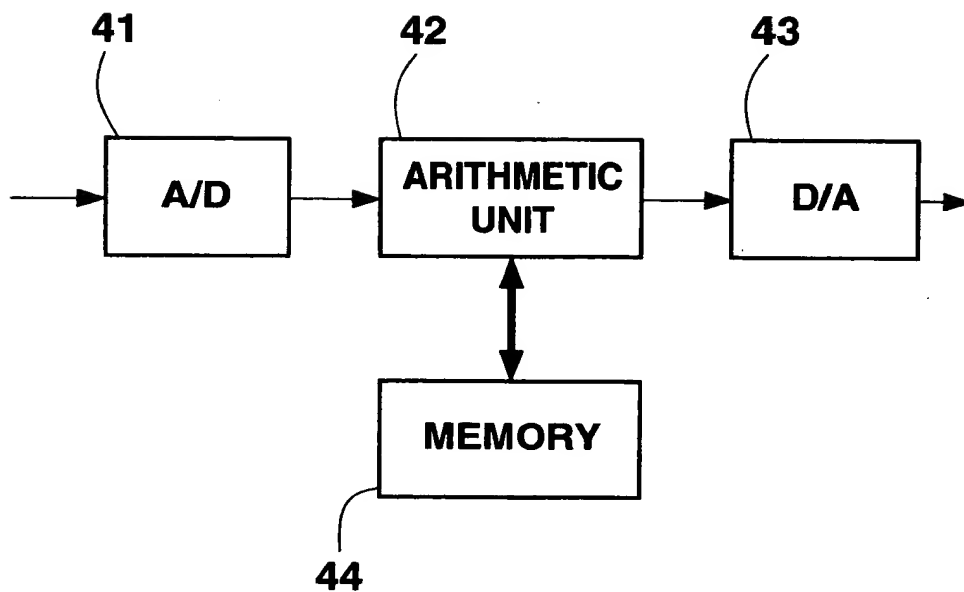


Fig. 4

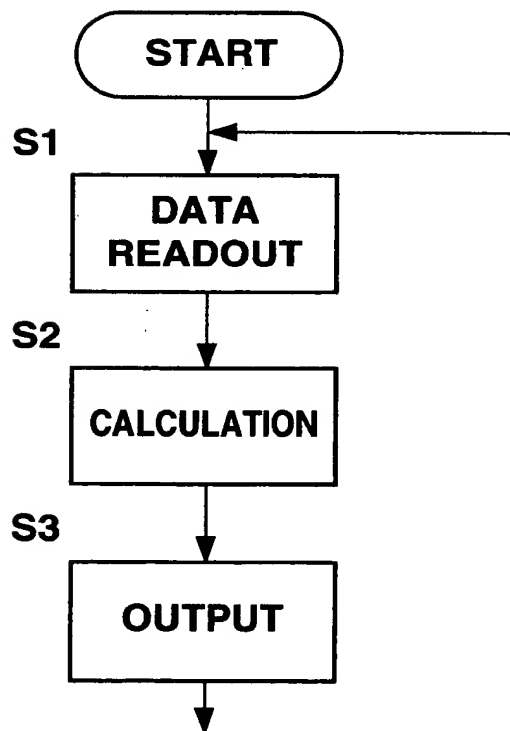


Fig. 5

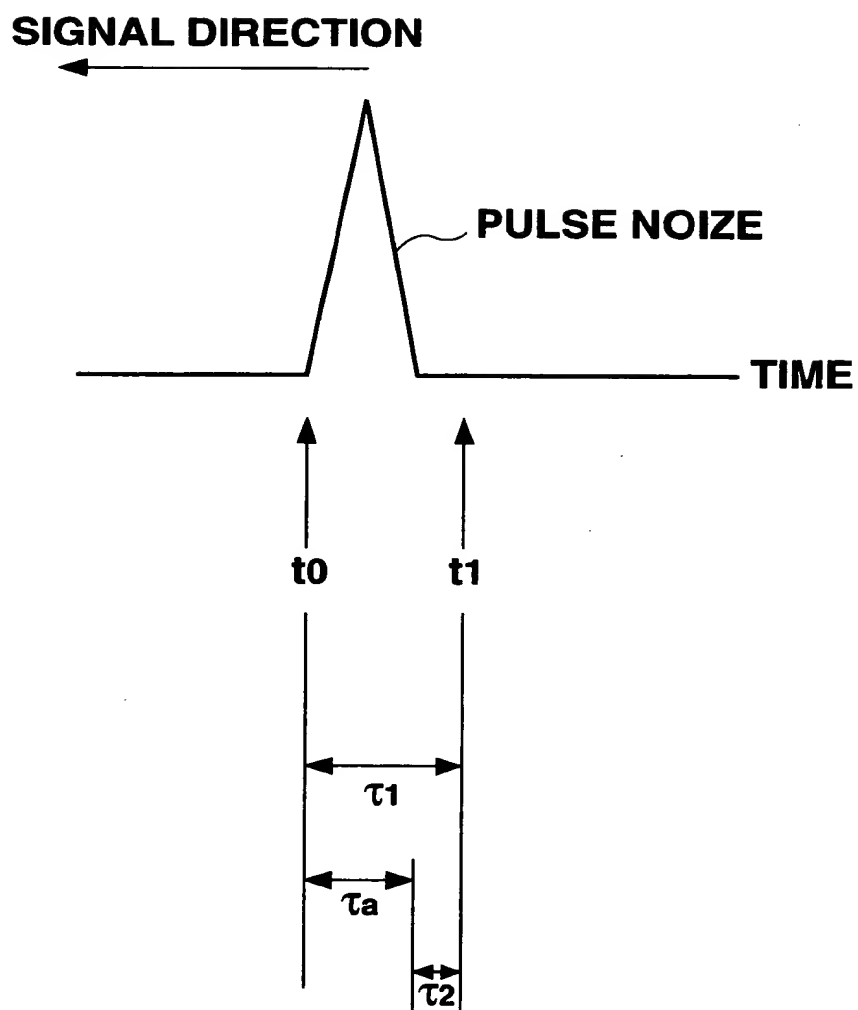


Fig. 6

(a) NOISE DETECTION LINE

SIGNAL DIRECTION →

τ_2

(b) INTERPOLATION LINE WITH DELAY

COMPLETE REMOVAL IS POSSIBLE

(c) INTERPOLATION LINE WITHOUT DELAY

NOISE REMAINS DUE TO INCOMPLETE REMOVAL

INTERPOLATED ←

TIMING OF DETECTION = TIMING OF THE SWITCHING OF THE SELECTION CIRCUIT 8

Fig. 7